

SECTION 2: FORM PTO 1449 - MODIFIED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#4
P.B.
11-27-01
1c971 U.S. PTO
09/932216
08/17/01

Applicant: Yang Atty Dkt: 2550/111
Serial No: Not yet assigned Date: August 17, 2001
Date Filed: Herewith
Invention: Bootstrap Module for Multi-Stage Circuit

Commissioner for Patents
Washington, DC 20231

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS'
INFORMATION DISCLOSURE STATEMENT

U.S. Documents

<u>Exam. Ref.</u>	<u>Document</u>	<u>Issue</u>	<u>Name</u>	<u>Class</u>
<u>Init.</u>	<u>No.</u>	<u>Number</u>	<u>Date</u>	
AA	6,060,937	May 9, 2000	Singer, et. al.	327/390
AB	6,118,326	Sept. 12, 2000	Singer, et. al.	327/390

Other Documents

<u>Exam. Ref.</u>	<u>Name</u>
<u>Init.</u>	<u>No.</u>
AC	"A 1.5-V, 10-bit, 14.3-MS/s CMOS Pipeline Analog-to-Digital Converter", Abo, et. al., IEEE Journal of Solid-State Circuit, Vol. 34, No. 5, pp. 599-605, May 1999.

Examiner: _____

Date Considered: _____

NOTE FOR EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance AND not considered. Include copy of this form with next communication to applicant.